Advertisement for recruitment of Project Staff in Special manpower development Programme for Chip to Systems development (SMDP-C2SD) project carried out by Dept. of ECE

NITMGH/ES/SMDP/C2SD/24/Vol.II/2021/1286   18.02.2021

Recruitment of the Project Staff through **walk-in Interview** (Lab Engineer) under the DeitY-Sponsored Project of 5 years duration (for remaining duration up to 30th November 2021).

Details about the positions

Sl	Post	Qualification	Duration	Consolidated Salary (PM)
1	Lab. Engineer No:01	M. Tech/M.E. in Microelectronics/VLSI with experience in CAD tools and Programming Language	As above	Rs. 34,000/= (Per month)

Duties and responsibilities:
1. Carrying out R&D work.
2. Assisting the students during the laboratory classes and project work.
3. Undergoing training on the advanced VLSI design topics under the Instruction Enhancement Programme (IEP) to be conducted at the VLSI man power resource centers.
4. Preparation and submission of project reports to the project funding and monitoring agencies.
5. Developing the skills on advanced VLSI CAD tools and guide the students on chip design under India chip program.
6. Maintaining the VLSI Lab, Extended VLSI Lab.
7. Any other duties that may be assigned by the project Investigators or Institute Authority.

Terms and conditions:
1. The above position is purely contractual and upto the 30th November 2021.
2. No TA/DA will be paid for attending the interview.
3. No accommodation will be provided by the Institute.
3. Candidates must send scan copies of filled-up application form, bio-data, passport photos, academic certificates, mark sheets, degrees and testimonials by **1st Mar 2021**. (Email: anup.dandapat@nitm.ac.in)
4. Candidates are required to attend online Interview**: Mar 03, 2021 at 10 AM.**
5. **Last date of sending application is Mar 01, 2021**.

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