

JRF Application Scrutiny Report

With the reference to advertisement No: NITMGH/ES/BRNS/EC/81/2018-19/636 dated: 18-06-2019, for the Junior Research Fellow (JRF) position at Mumbai location in the BRNS, sponsored Research Project "Post-Processing of NQR signals in digital domain in FPGA", undertaken by Department of Electronics and Communication Engineering, NIT Meghalaya. The total number of application/bio-data have been received till 10/07/2019 are 12 and list of shortlisted candidates calling for personal interview are 12 to the venue.

Shortlisted applicants will appear for a personal interview to be held as given below:

Reporting Time & Date	9:00 AM on 15 th July 2019
Venue	Conference Room, Training School Hostel, Anushaktinagar, Mumbai 400094.

- 1) Candidates must bring a copy of call letter.
- 2) Candidates have to produce all their original certificates/ proof of date of birth before the interview for verification.
- 3) Please bring four passport photographs along with a hard copy of filled application form and self- attested photocopies of all certificates.
- 4) No TA/DA will be paid for attending the interview.
- 5) No Accommodation has been provided for attending the interview

List of Shortlisted Candidates for Interview :

Sno	Name	Specialization
1	Rachana Khandelwal	B.E, EEE
2	Vineet Kumar	B.Tech, ECE
3	Alok Kumar Singh	B.Tech, EIE
4	Gurgeet Singh Bhogal	B.Tech, ECE
5	P.N.V.Naveen Kumar	B.Tech, EIE
6	Srishti Ramavat	B.E, ECE
7	Pulkit Saxena	M.Sc, Electronics
8	Vishal Raj	B.E, ECE

9	Kishan Naresh Patel	B.E, ECE
10	Sumeet Mukesh Kaydalwar	B.E, (ECE)
11	Tarit Sarkar	M.Sc, Electronics
12	Prathamesh Purnanand Sarode	B.E,(ECE)

P. Rangababu
11/07/19

Dr.P.Rangababu
Member(PI)

Dr.P K.Rathore
Member

Ch. V. Ramarao
11/07/19

Dr.Ch.V Ramarao
(Co PI)